



## Product News

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# IAR Systems updates RISC-V development tools with support for RV32E and Atomic operations

Uppsala, Sweden—December 3, 2019—IAR Systems®, the future-proof supplier of software tools and services for embedded development, announces that a new version of the toolchain IAR Embedded Workbench® for RISC-V is now available. Version 1.20 adds support for the base instruction set RV32E and also the standard extension for Atomic operations (A).

One of the major benefits of using RISC-V is the flexibility the architecture provides, which enables OEMs as well as SoC vendors to design custom cores with the exact definitions needed for the application or product. By continuously adding more support and functionality, IAR Systems makes it possible for these companies to make full use of the capabilities of the leading embedded development toolchain for developing applications based on custom cores.

Through excellent optimization technology, IAR Embedded Workbench helps developers ensure the application fits the required needs and optimize the utilization of on-board memory. Version 1.20 of IAR Embedded Workbench for RISC-V adds support for the base instruction set RV32E that targets smaller embedded devices with the register set reduced to half of what is available in RV32I. The standard extension for Atomic operations (A) adds instructions that support atomic read, modify, and write actions to support synchronization between different HW processes that access the same memory.

“The more features we add to IAR Embedded Workbench for RISC-V, the more designers can benefit from fully utilizing the quality, reliability and performance that distinguish our complete IDE,” comments Anders Holmberg, Chief Strategy Officer, IAR Systems. “If you add that we are able to offer professional support and training with access to our specialists, you get a powerful tool with excellent support, much appreciated by developers all over the world”.

RISC-V is a free and open instruction set architecture (ISA) based on established Reduced Instruction Set Computing (RISC) principles. In May 2019, IAR Systems released the first version of IAR Embedded Workbench for RISC-V. Complementing its strong tools product offering, the company delivers renowned technical support from offices around the globe.

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IAR Systems is exhibiting at the RISC-V Summit next week, December 10-11 in San José, California, and will be demoing IAR Embedded Workbench for RISC-V. More information is also available at [www.iar.com/riscv](http://www.iar.com/riscv).

### Ends

*Editor's Note: IAR Systems, IAR Embedded Workbench, Embedded Trust, C-Trust, IAR Connect, C-SPY, C-RUN, C-STAT, IAR Visual State, IAR KickStart Kit, I-jet, I-jet Trace, I-scope, IAR Academy, IAR, and the logotype of IAR Systems are trademarks or registered trademarks owned by IAR Systems AB. All other product names are trademarks of their respective owners.*

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#### **About IAR Systems**

IAR Systems supplies future-proof software tools and services for embedded development, enabling companies worldwide to create the products of today and the innovations of tomorrow. Since 1983, IAR Systems' solutions have ensured quality, reliability and efficiency in the development of over one million embedded applications. The company is headquartered in Uppsala, Sweden and has sales and support offices all over the world. Since 2018, Secure Thingz, a provider of advanced security solutions for embedded systems in the IoT, is part of IAR Systems. IAR Systems Group AB is listed on NASDAQ OMX Stockholm, Mid Cap. Learn more at [www.iar.com](http://www.iar.com).