

## **Product News**

Date: March 10, 2015

## IAR Systems shortens build times in leading development toolchain for ARM-based devices

Latest version of IAR Embedded Workbench for ARM adds new device support as well as parallel build and integrated static code analysis

Uppsala, Sweden—March 10, 2015—IAR Systems® updates its leading development tools for ARM® with new device support as well as new functionality. Version 7.40 of IAR Embedded Workbench® for ARM introduces support for ARM Cortex®-M7 microcontrollers from STMicroelectronics and Atmel. In addition, the tools now feature parallel build for shorter build times, as well as an integration of IAR Systems' new tool C-STAT® for powerful static code analysis.

The ARM Cortex-M7 processor is the most recent addition to the ARM Cortex-M family. It is focused on energy efficiency and high performance and is intended for use in a wide variety of applications including for example automotive, industrial automation, medical devices and the Internet of Things. The new version of IAR Embedded Workbench adds device support for ARM Cortex-M7 devices from Atmel and STMicroelectronics, including support for the double precision floating point unit. The support covers the Atmel | SMART™ MCUs ATSAMx70. From STMicroelectronics, the STM32F756 devices are now supported. In addition to these new ARM Cortex-M7 microcontrollers, support for a number of ARM Cortex-based devices from several other vendors has been added.

In order to speed up build times, version 7.40 introduces parallel build. The user can easily set the compiler to run in several parallel processes and make better use of the available processor cores in the PC. This feature can have a major impact on reducing the build times of the compiler.

The add-on product C-STAT for powerful, integrated static code analysis is now available. Static analysis finds potential issues in code on the source code level and can be used to prevent errors such as memory leaks, access violations, arithmetic errors and array and string overruns. The analysis performed by C-STAT improves code quality and aids alignment with industry coding standards. It checks compliance with rules as defined by MISRA C:2004, MISRA C++:2008 and MISRA C:2012, as well as hundreds of rules based on for example CWE (the Common Weakness Enumeration) and CERT C/C++. Users can easily select the rule set or individual rules to check their code against, and the analysis results are provided directly in the IAR Embedded Workbench IDE.

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More information about the complete C/C++ compiler and debugger toolchain IAR Embedded

Workbench is available at www.iar.com/ewarm.

**About MISRA C** 

MISRA, The Motor Industry Software Reliability Association, is a collaboration between vehicle

manufacturers, component suppliers and engineering consultancies which seeks to promote best

practice in developing safety-related electronic systems in road vehicles and other embedded systems.

MISRA C is a software development standard for the C programming language developed by MISRA.

More information is available at www.misra.org.uk

About CERT C/C++

The CERT C/C++ Secure Coding Standards are standards published by the Computer Emergency

Response Team (CERT) providing rules and recommendations for secure coding in the C/C++

programming languages. More information is available at www.cert.org

### Ends

Editor's Note: IAR Systems, IAR Embedded Workbench, C-SPY, C-RUN, C-STAT, visualSTATE, Focus on Your

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**About IAR Systems** 

IAR Systems provides developers of embedded systems with world-leading software tools for

developing competitive products based on 8-, 16-, and 32-bit processors. Established in Sweden in

1983, the company has over 46,000 customers globally, mainly in the areas of industrial automation,

medical devices, consumer electronics, telecommunication, and automotive products. IAR Systems has an extensive network of partners and cooperates with the world's leading semiconductor vendors. IAR

Systems Group AB is listed on NASDAQ OMX Stockholm. For more information, please visit

www.iar.com.